

Applicant : Andrew J. Burs et al.  
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*31*  
76. (New) The chip of claim *30* 75, wherein the first terminal is an input terminal and the second terminal is an intermediate terminal.

*32*  
77. (New) The chip of claim *32* 67, wherein the first and second pluralities of doped regions are n+ regions formed in a p-type well or substrate.

*33*  
78. (New) The chip of claim *32* 77, wherein the first terminal is a ground terminal and the second terminal is an intermediate terminal.

#### REMARKS

Applicant submits that the claims are in condition for examination.

Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 6/26/01

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